

2818



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: **Masazumi Amagai, Et al.**  
Serial No.: **10/008,049**  
Filed: **11/13/2001**  
For: **Copper-Based Chip Attach for Chip-Scale Semiconductor Packages**

Docket No.: **TI-33622**  
Examiner: **Le,Dung Anh**  
Art Unit: **2818**  
Conf. No.: **2810**

*6/ Elect*  
*C. S. Sta*  
*10-24-*

ELECTION

October 14, 2002

Assistant Commissioner For Patents  
Washington, D.C. 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that on this day the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner For Patents, Washington, D.C. 20231.

*Elizabeth Austin*  
Elizabeth Austin

*10/15/2002*  
Date

This election is offered in response to the Examiner's restriction requirement dated October 2, 2002. The Examiner has required restriction to one of the groups of: Group I, Claims 1-17, or Group II, Claims 18-21.

Applicant hereby elects to pursue Group I, Claims 1-17 without traversing the Examiner's restriction requirement.

Respectfully submitted,

*Michael K. Skrehot*

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